

AUS920030471US1

Dierks et al.

Method and Apparatus for Transferring Data From a Memory Subsystem to a Network Adapter for Improving the Memory Subsystem and PCI Bus Efficiency

1/4

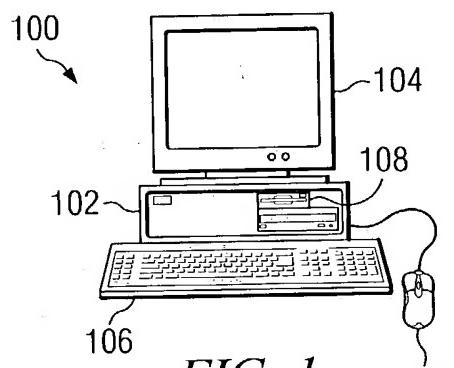
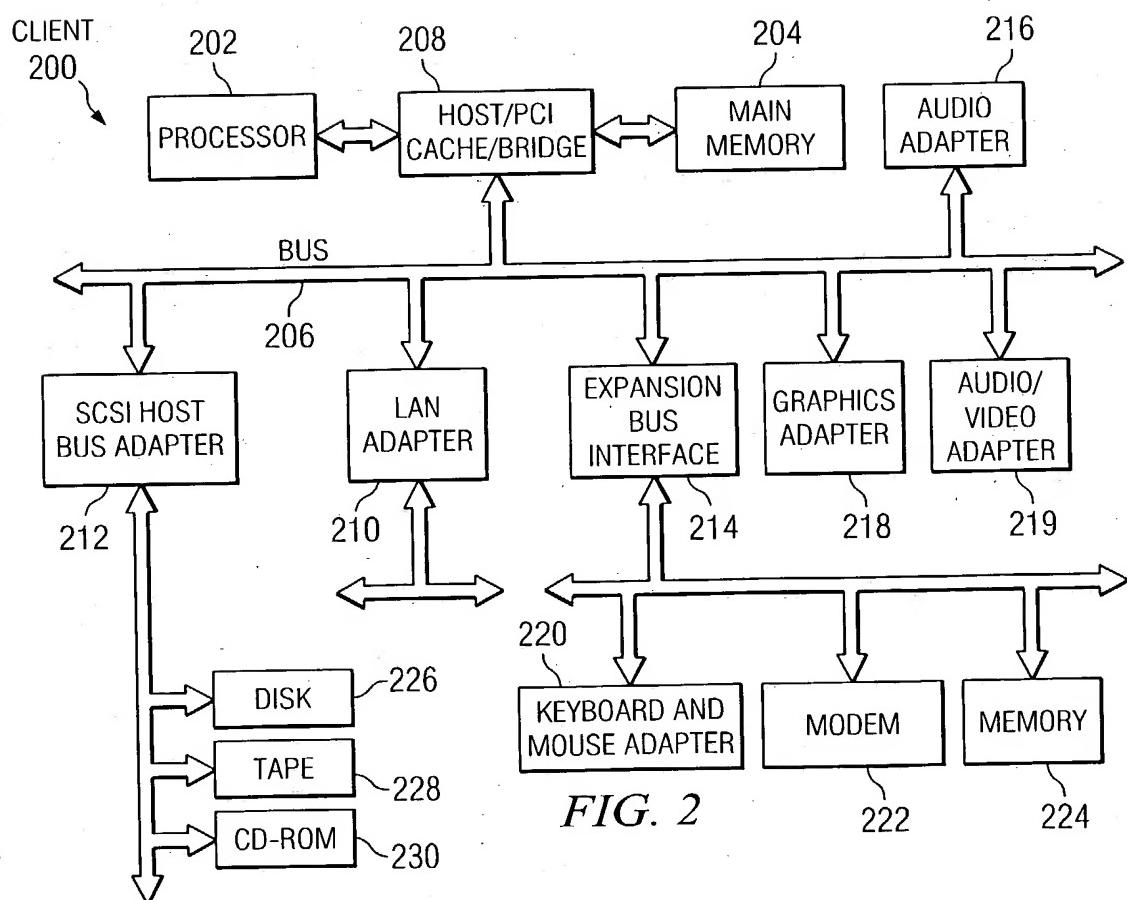


FIG. 1



AUS920030471US1

Dierks et al.

Method and Apparatus for Transferring Data From a Memory Subsystem to a Network Adapter for Improving the Memory Subsystem and PCI Bus Efficiency

2/4

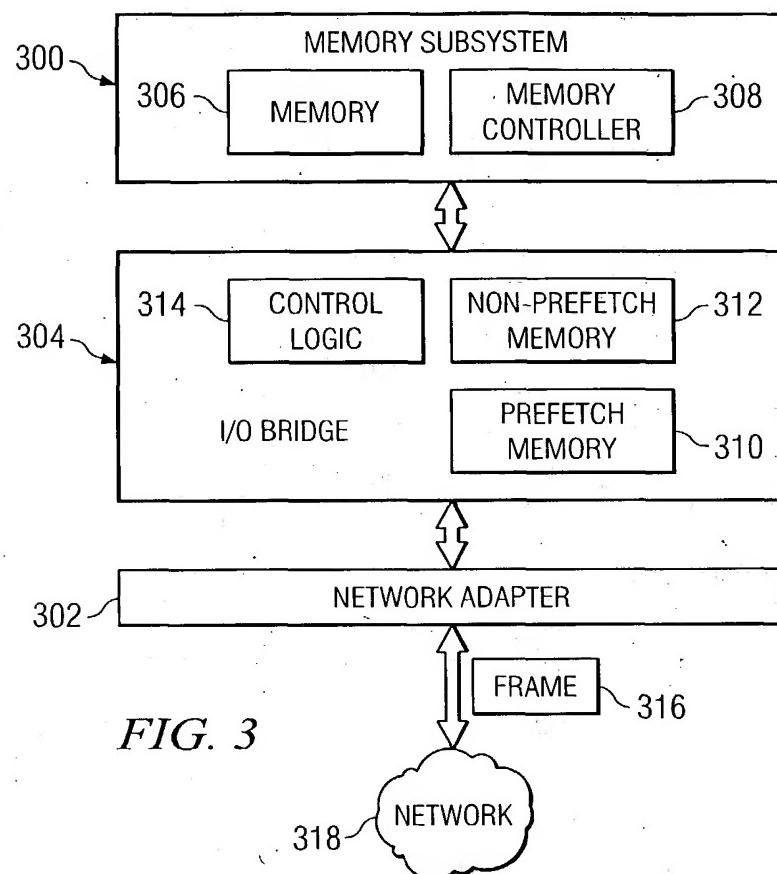


FIG. 3

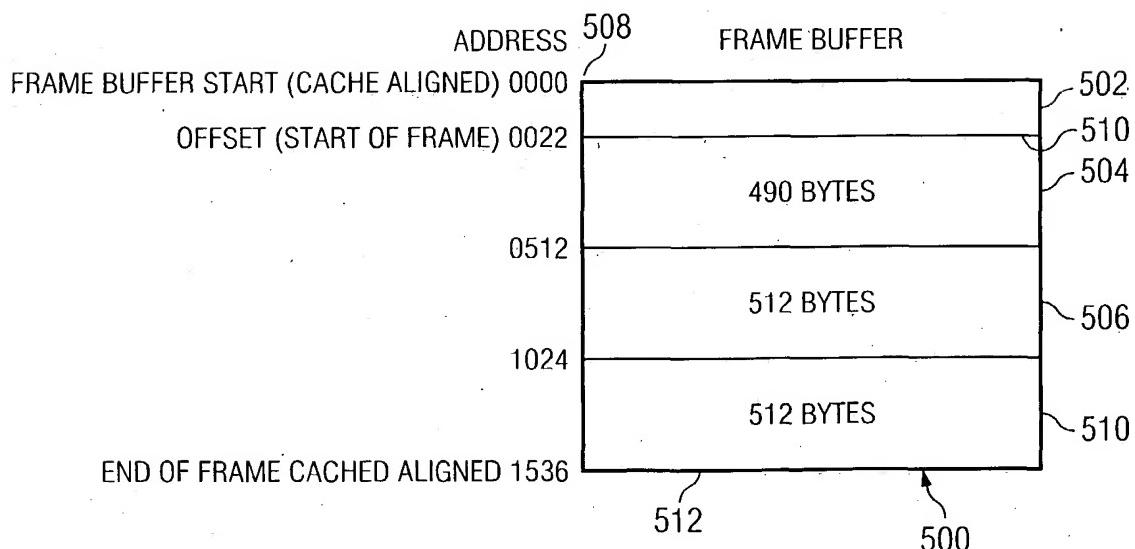


FIG. 5

AUS920030471US1

Dierks et al.

Method and Apparatus for Transferring Data From a Memory Subsystem to a Network Adapter for Improving the Memory Subsystem and PCI Bus Efficiency

3/4

PCI COMMAND	NUMBER OF BYTES TRANSFERRED	CUMULATIVE TOTAL
MRM	512	512
MRM	512	1024
MRM	384	1408
MR	8	1416
MR	8	1424
MR	8	1432
MR	8	1440
MR	8	1448
MR	8	1456
MR	8	1464
MR	8	1472
MR	8	1480
MR	8	1488
MR	8	1496
MR	8	1504
MR	8	1512
MR	2	1514

*FIG. 4  
(PRIOR ART)*

PCI COMMAND	NUMBER OF BYTES TRANSFERRED	CUMULATIVE TOTAL
MRM	490	490
MRM	512	1002
MRM	512	1514

*FIG. 6*

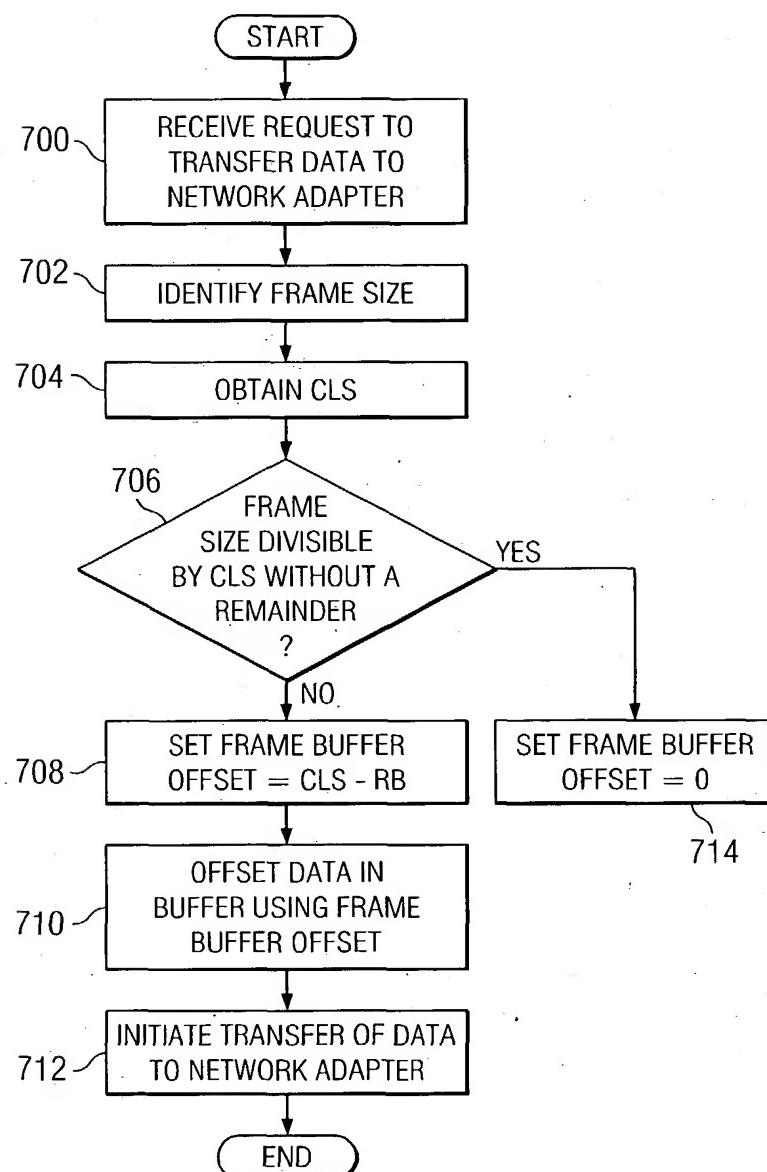


FIG. 7